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Application Number	10722,691
Filing Date	November 24, 2003
First Named Inventor	Indeck et al.
Art Unit	2162
Examiner Name	Fleurantin
Attorney Docket Number	53047-44791

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
/MF		US- 5,819,290	10/6/1988	Fujita	
/MF		US- 5,339,411	8/16/1994	Heaton, Jr.	

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
/MF	AA	EP 0 911 738 A	4/28/1999	Calluna Technology Ltd.		
/MF	AB	WO 01/22425 A	3/29/2001	Seagate Technology LLC		
/MF	AC	EP 0 851 358 A	7/1/1998	Sun Microsystems		

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
/MF	AD	PATENT COOPERATION TREATY; International Search Report; April 12, 2005	
/MF	AE	COMPTON et al.; <i>Configurable Computing: A Survey of Systems and Software</i> ; Technical Report, Northwestern University, Dept. of ECE, 1999	
/MF	AF	SCHMIT; <i>Incremental Reconfiguration for Pipelined Applications</i> ; Dept. of ECE, Carnegie Mellon University 1997, Pittsburgh, PA	
/MF	AG	ANONYMOUS; <i>Method for Allocating Computer Disk Space to a File of Known Size</i> , IBM Technical Disclosure Bulletin, Vol. 27, No. 10B, March 1, 1985, New York	
/MF	AH	NUNEZ et al.; <i>The X-MatchLITE FPGA-Based Data Compressor</i> , Euromicro Conference 1999, Proceedings, Italy, Sept. 8-10, 1999, Los Alamitos, CA	
/MF	AI	GUNTHER et al.; <i>Assessing Document Relevance with Run-Time Reconfigurable Machines</i> ; FPGAs for custom computing machines, 1996; Proceedings, IEEE Symposium on Napa Valley, CA, April 17, 1996	
/MF	AJ	HAUCK et al.; <i>Software Technologies for Reconfigurable Systems</i> , Northwestern University, Dept. of ECE, Technical Report, 1996	
/MF	AK	EBELING et al.; <i>RaPiD – Reconfigurable Pipelined Datapath</i> , University of Washington, Dept. of Computer Science and Engineering, September 23, 1996; Seattle, WA	
/MF	AL	PRAMANIK et al.; <i>A Hardware Pattern Matching Algorithm on a Dataflow</i> , Computer Journal, Oxford University Press, July 1, 1985; East Lansing, MI	

Examiner
Signature

/ s / Fleurantin

Date
Considered

7-11-05

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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